

**SECRET**

## Application Type:: Regular

Subject Matter::	Utility
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**Suggested Classification::**

**Suggested Group Art Unit::**

CD-ROM or CD-R?: None

Title:: Arrangement for Measuring Pressure on a Semiconductor Wafer and an Associated Method for Fabricating a Semiconductor Wafer

Request for Early Publication?: No

Request for Non-Publication?:: Yes

Suggested Drawing Figure:: 53

Total Drawing Sheets:: 15

Small Entity::	No
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Petition included?: No

Secrecy Order in Parent Appl.?: No

Applicant Authority type:: **Inventor**

Primary Citizenship Country: US

Status:: Full Capacity

Given Name: Newell E.

Family Name:: Chiesl

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State if Province of Residence:: CO

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**Correspondence Information**

Correspondence Customer Number::



**Representative Information**

Representative Customer Number:



**Domestic Priority Information**

Application::	Continuity Type:	Parent Application::	Parent Filing Date::

**Assignee Information**

Assignee name:: LSI Logic Corporation  
Street of mailing address:: 1551 McCarthy Boulevard  
City of mailing address:: Milpitas  
State or Province of mailing address:: CA  
Country of mailing address:: USA  
Postal or Zip Code of mailing address:: 95035